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Examiner Name	Unknown <u>PAUL DINH</u>	
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Title: IDENTIFYING LINE WIDTH ERRORS IN INTEGRATED CIRCUIT DESIGNS		

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Examiner Signature	<u>Paul Dinh</u>	Date Considered	<u>10/17/05</u>
<small>*Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</small>			
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